

GA-F2A85XM-HD3

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05	APU CONTROL
06	APU UMI, GFX, GPP
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08	DDRIII CHANNEL A
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11	HUDSON SATA,SPI,HWM
12	HUDSON POWER & GND
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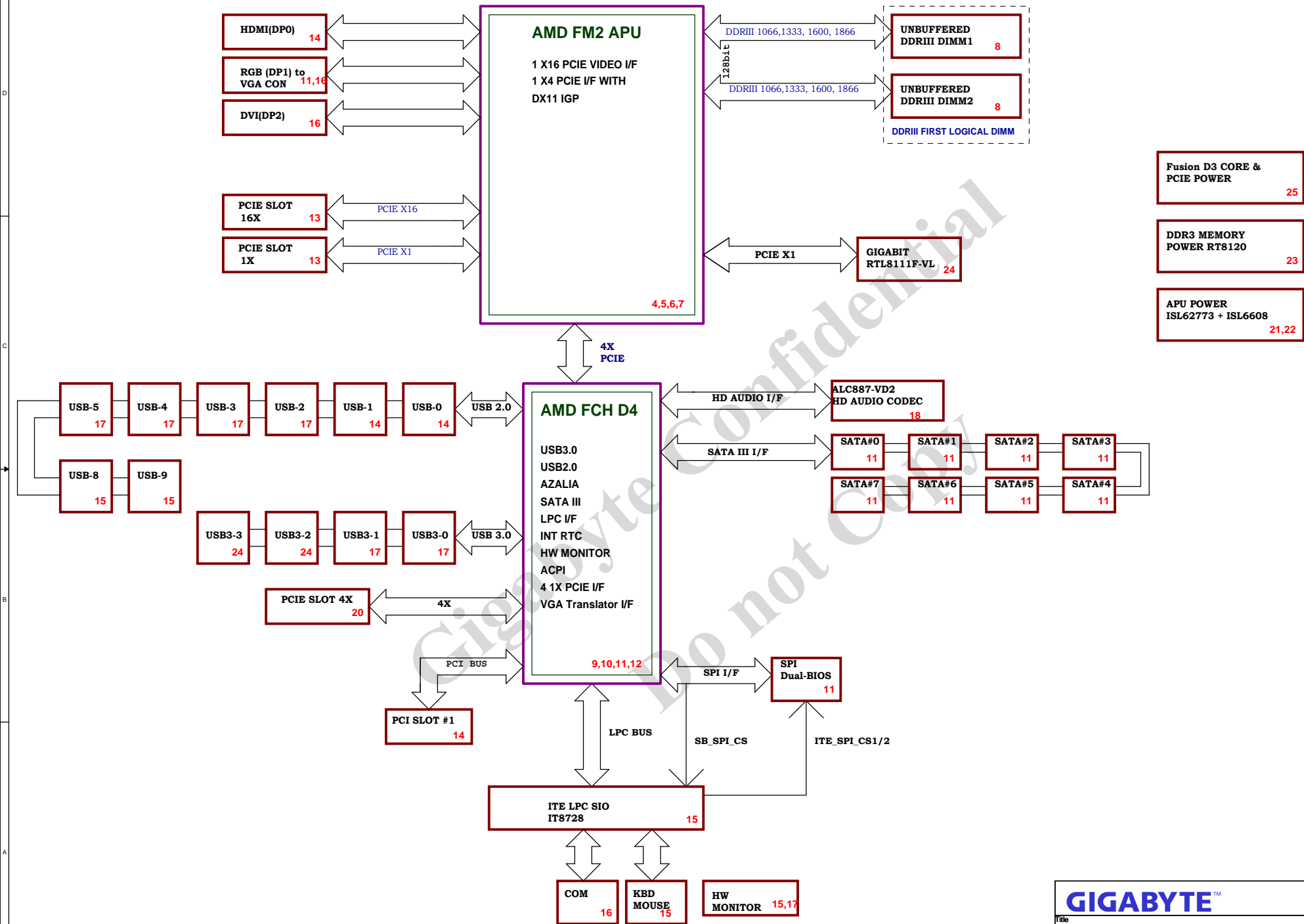
APU_VDDIO_SUS=DDR15V
APU_VTT_SUS=DDRVTT
APU_VDDP_RUN=APU_VDDR_RUN=APU_VDDP

+1.1V_RUN=FCH_VDD_11_RUN=VCC_SB

+3.3V_RUN=VCC3
+3.3V_ALW=3VDUAL

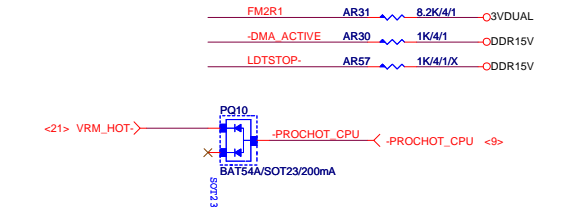
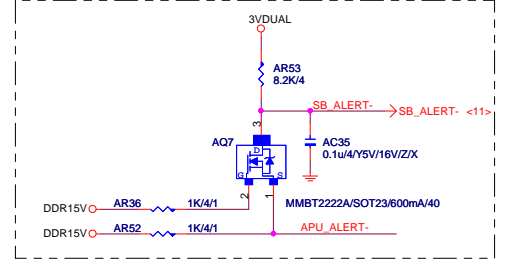
Gigabyte Confidential
Do not Copy

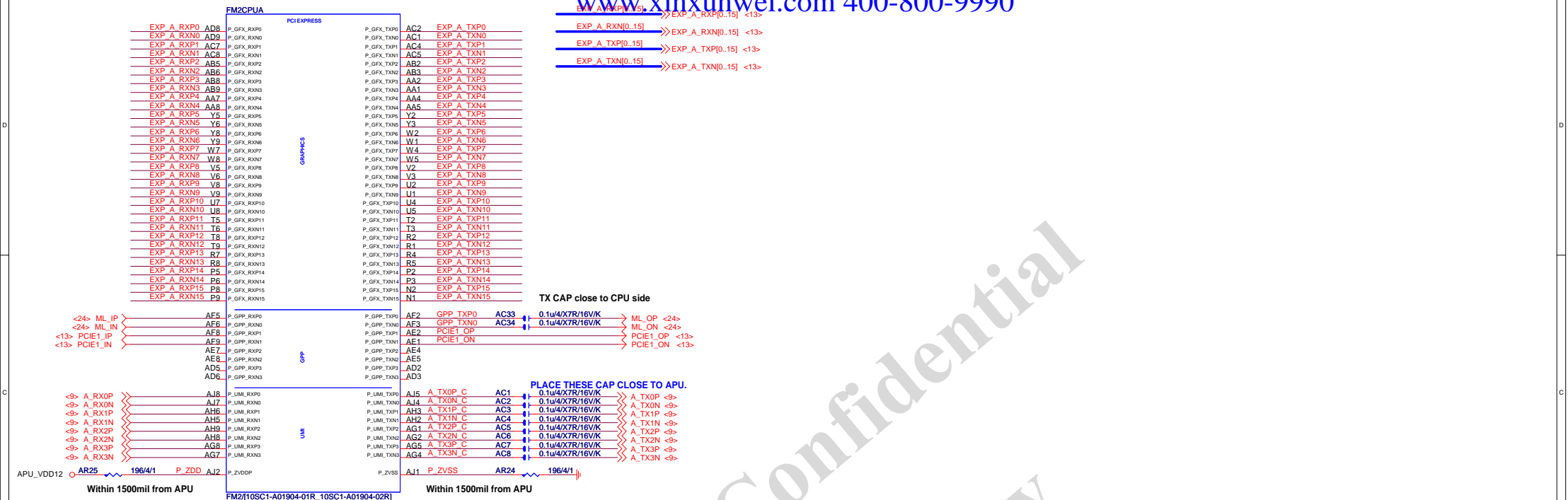
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**GIGABYTE™****BLOCK DIAGRAM**

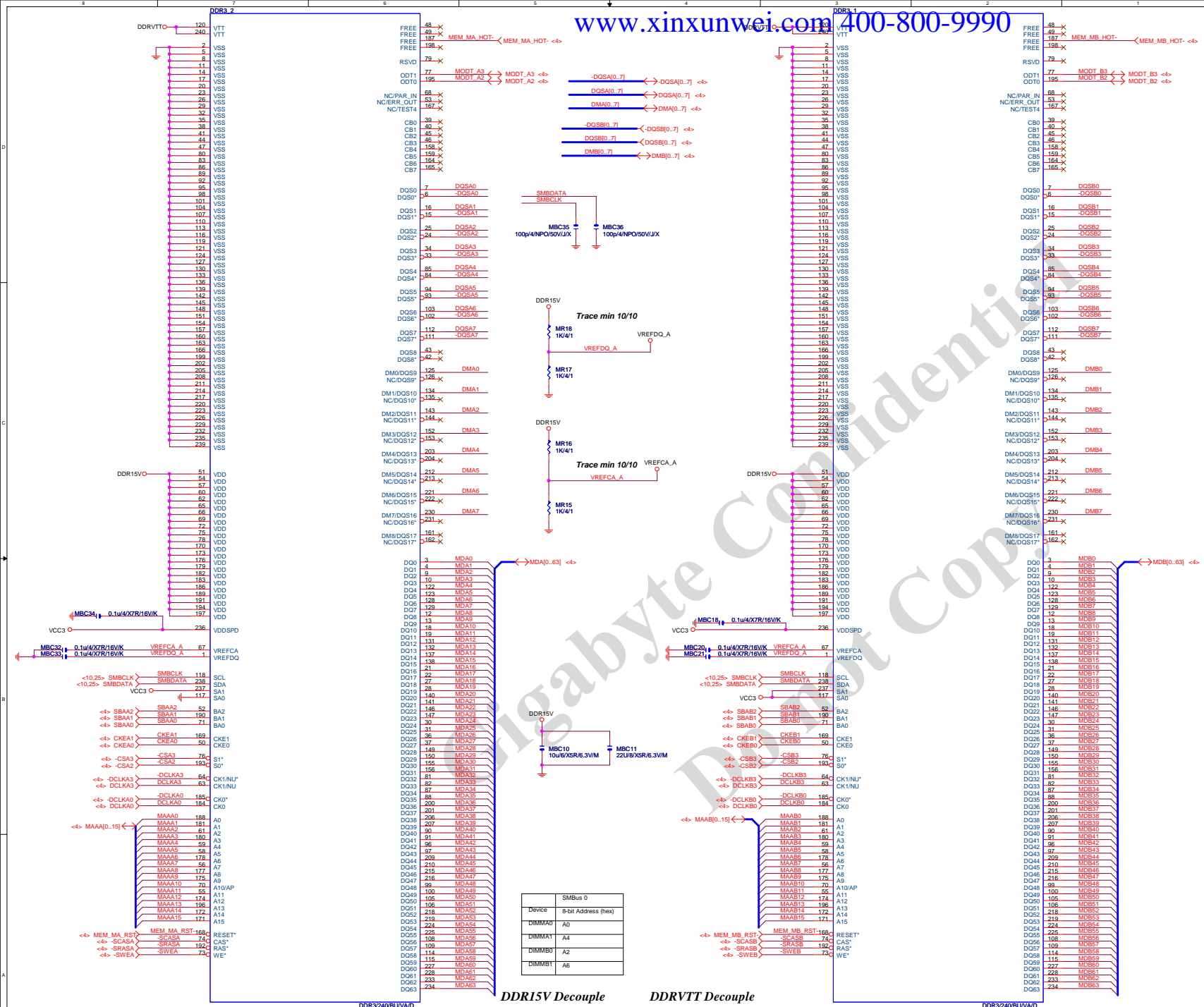
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Custom		
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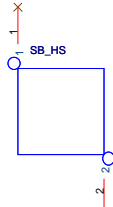




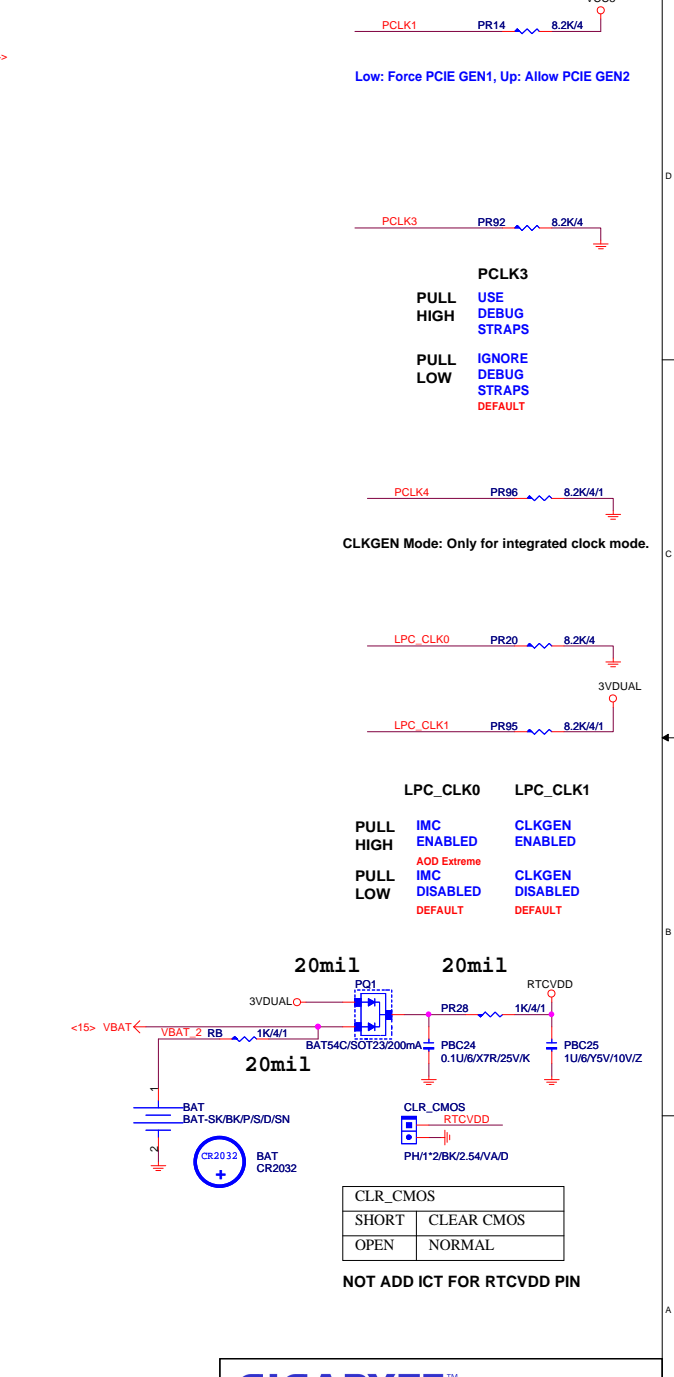
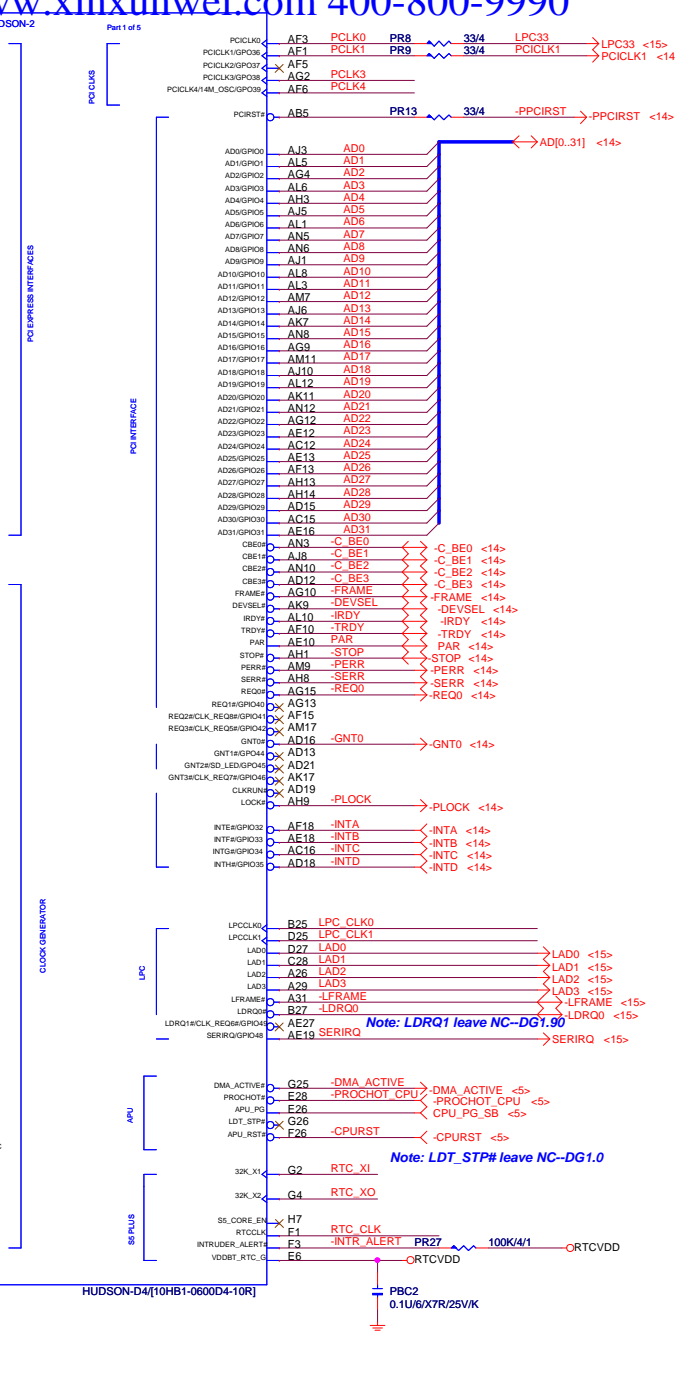
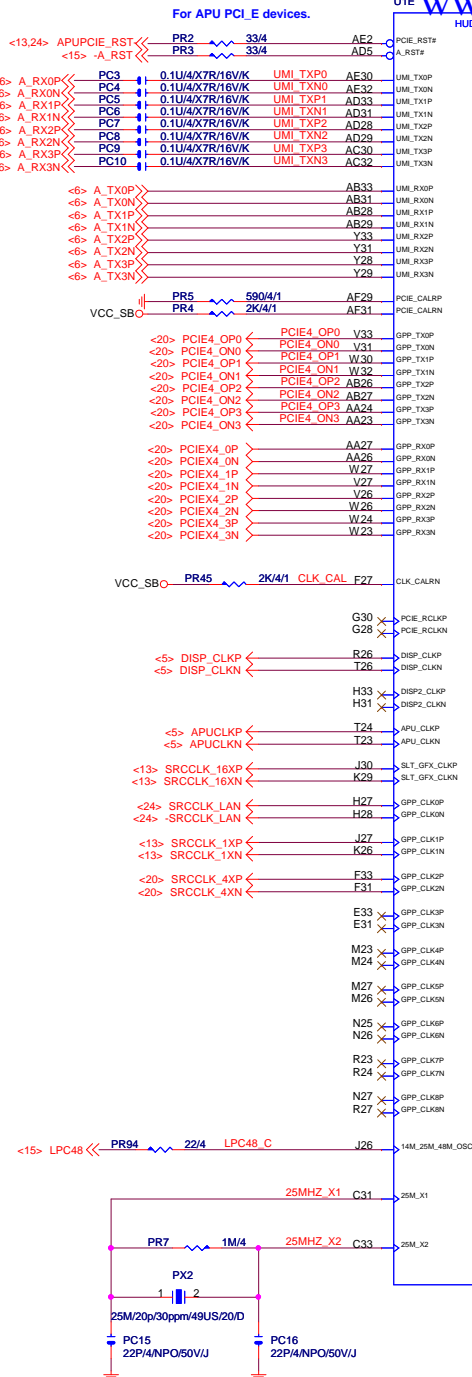
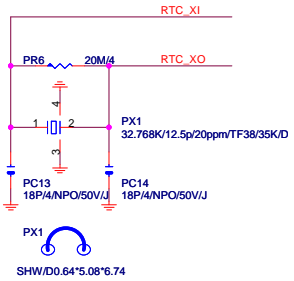


PLACE THESE PCIE AC COUPLING
CAPS CLOSE TO SB850

S.B HEATSINK



SB_HS[12SP2-SA0301-01R_12SP2-SA0301-02R_12SP2-SA0301-03R]



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Hudson D4 PCIE/PCI/CPU/LPC

Size Custom

Document Number

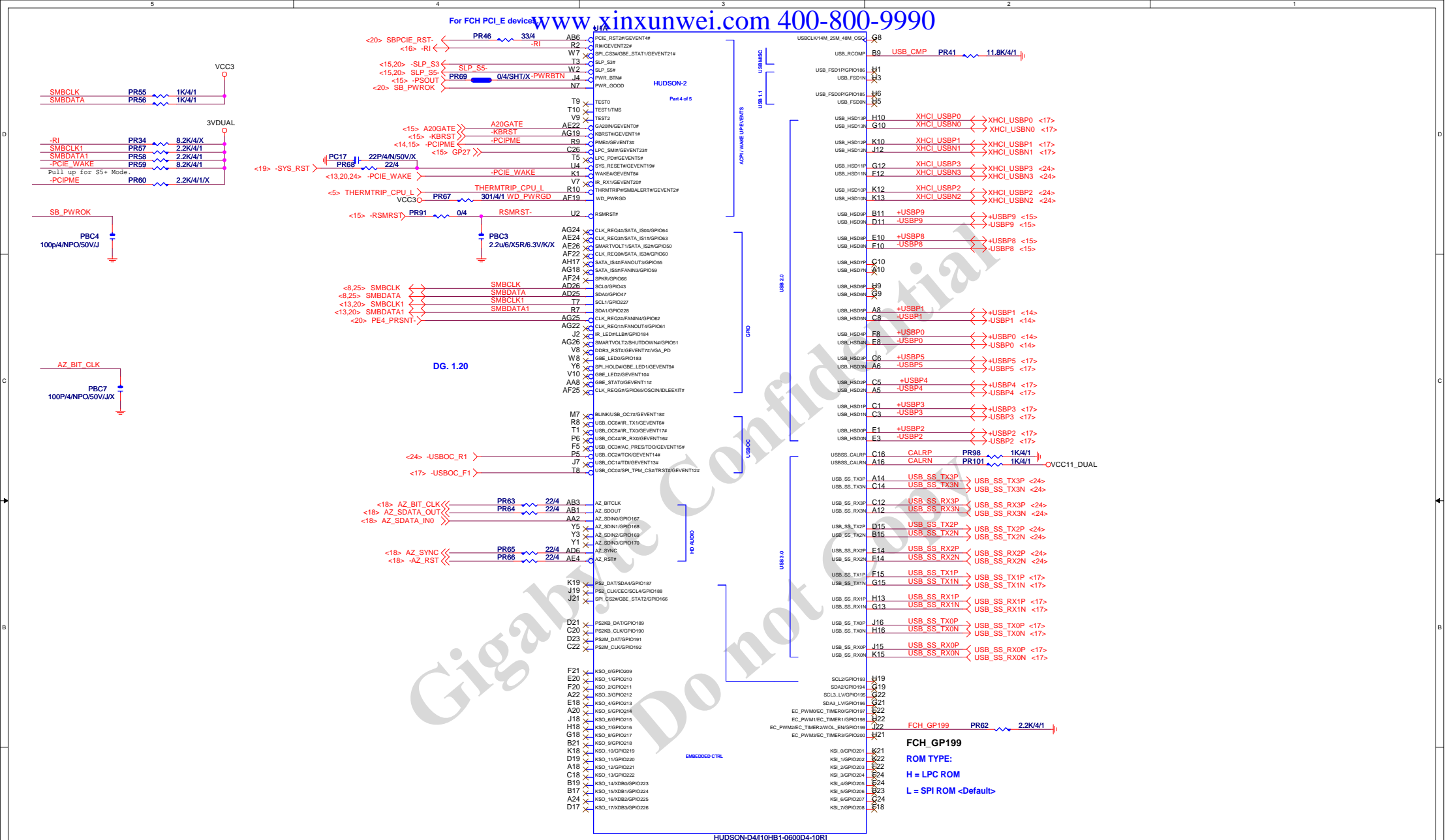
Rev 1.01

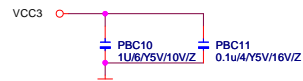
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CLR_CMOS	
SHORT	CLEAR CMOS
OPEN	NORMAL

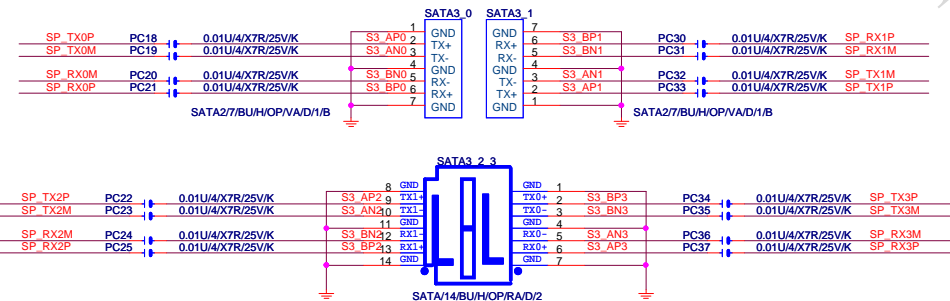
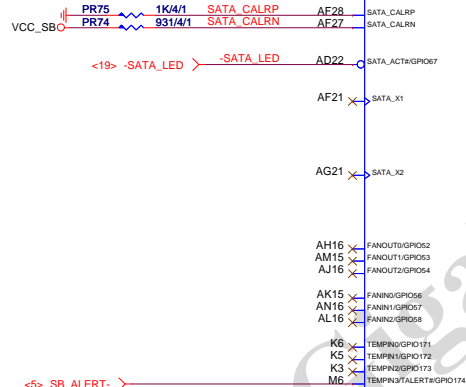
NOT ADD ICT FOR RTCVDD PIN





SATA 6~7 for Hudson D4.

PLACE SATA_CAL RES VERY CLOSE TO BALL OF U1



Part 2 of 5

SD_CLK#GCLK_2/GPIO73 AL14
SD_CMD#GCLK_2/GPIO74 AN14
SD_CD#GPIOW75 AJ12
SD_WP#GPIOW76 AH12
SD_DATA0SDAT1_2/GPIO77 AK13
SD_DATA1SDAT0_2/GPIO78 AM13
SD_DATA2GPIOW79 AH15
SD_DATA3GPIOW80 AJ14

GBE_COL AC4
GBE_CRS AD3
GBE_MDC# AD9
GBE_MDIO W10
GBE_RXCLK AB8
GBE_RXD0 AH7
GBE_RXD2 AF7
GBE_RXD1 AE7
GBE_RXD0 AD7
GBE_RXCTLRXDV AG8
GBE_RXEN# AD1
GBE_TXCLK AB7
GBE_TXD3 AF9
GBE_TXD2 AG6
GBE_TXD1 AE9
GBE_TXD0 AD8
GBE_TXCTLTXEN AB9
GBE_PHY_PD AC2
GBE_PHY_RST# AA7
GBE_PHY_INT# W3

SPL_DV#GPIOW84 V6 SB SPI DI R PR70 22/4 SB SPI DI
SPL_DV#GPIOW85 V5 SB SPI DO R PR71 22/4 SB SPI DO
SPL_CLK#GPIOW86 V3 SB SPI CLK R PR72 22/4 SB SPI CLK
SPL_CS1#GPIOW87 T6 SB SPI CS- PR73 22/4 SB SPI CS_ITE

VGA_RED L30 PR44 150/4/1 DAC_RED <16>
VGA_GREEN L32 PR12 150/4/1 DAC_GREEN <16>
VGA_BLUE M29 PR10 150/4/1 DAC_BLUE <16>

VGA_HSYNCGPIOW88 M28 DAC_HSYNC DAC_HSYNC <16>
VGA_VSYNCGPIOW89 N30 DAC_VSYNC DAC_VSYNC <16>
VGA_DDC_SDVGPIOW90 M33 DDCDATA DDCDATA <16>
VGA_DDC_SCLGPIOW91 N32 DDCCLK DDCCLK <16>

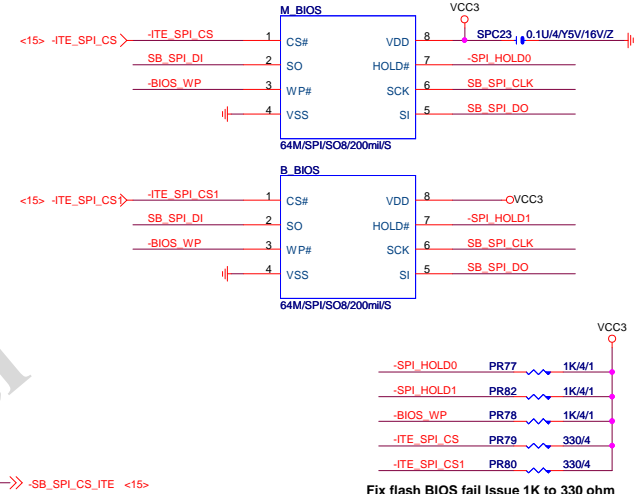
VGA_DAC_RSET K31 DAC_RSET PR87 715/4/1
AUX_VGA_CH_F V28 VGA CHP PC11 0.1U/4X7R/16V/K DP1_AUXP <5>
AUX_VGA_CH_L V29 VGA CHN PC12 0.1U/4X7R/16V/K DP1_AUXN <5>

AUX_CAL U28 AUX_CAL PR99 100/4/1 VCC_SB
ML_VGA_LUP T31 SW VGA TXD0+ HC19 0.1U/4X7R/16V/K DP1_TXP0 <5>
ML_VGA_LDN T33 SW VGA TXD0- HC20 0.1U/4X7R/16V/K DP1_TXN0 <5>
ML_VGA_LUP T29 SW VGA TXD1+ HC15 0.1U/4X7R/16V/K DP1_TXP1 <5>
ML_VGA_LDN T28 SW VGA TXD1- HC16 0.1U/4X7R/16V/K DP1_TXN1 <5>
ML_VGA_LUP R32 SW VGA TXD2+ HC17 0.1U/4X7R/16V/K DP1_TXP2 <5>
ML_VGA_LDN P29 SW VGA TXD2- HC18 0.1U/4X7R/16V/K DP1_TXN2 <5>
ML_VGA_LUP P28 SW VGA TXD3+ HC21 0.1U/4X7R/16V/K DP1_TXP3 <5>
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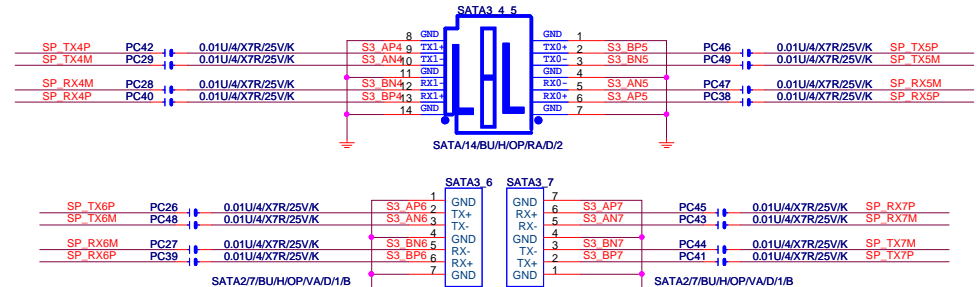
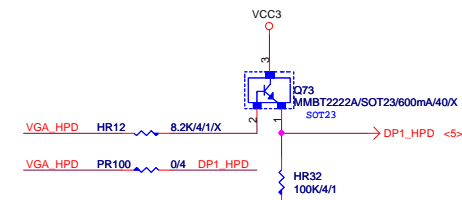
ML_VGA_HPDPGPIOW92 C29 VGA HPD
VIN0GPIOW176 N2
VIN1GPIOW176 M3
VIN2SDAT1_1GPIOW177 L2
VIN3SDAT0_1GPIOW178 N4
VIN4SLOAD_1GPIOW179 P1
VIN5CLK_1GPIOW180 P3
VIN6GBE_STAT3GPIOW181 M1
VIN7GBE_LED3GPIOW182 M5

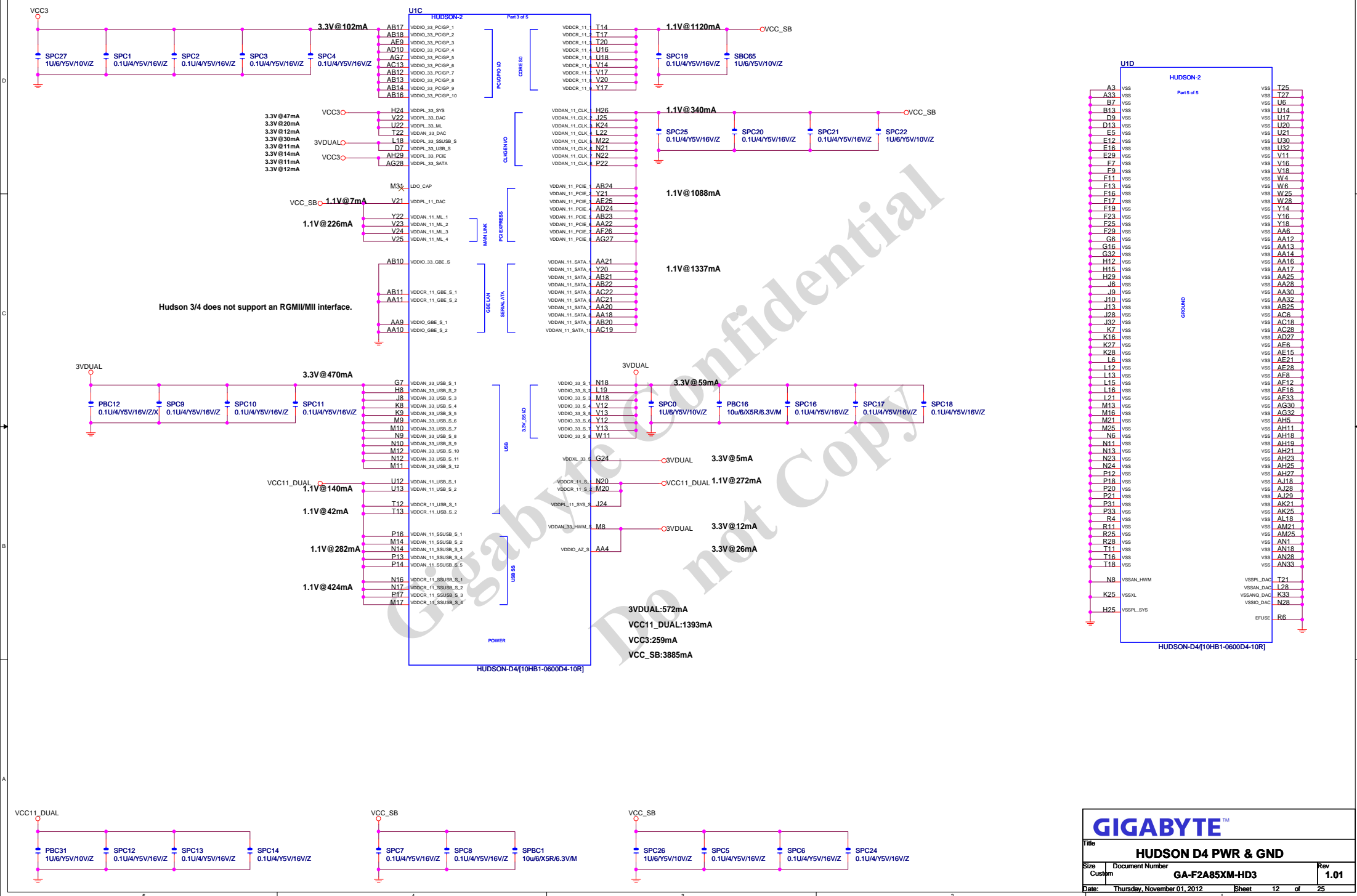
NC1 AG16
NC2 AH10
NC3 A28
NC4 G27
NC5 L4

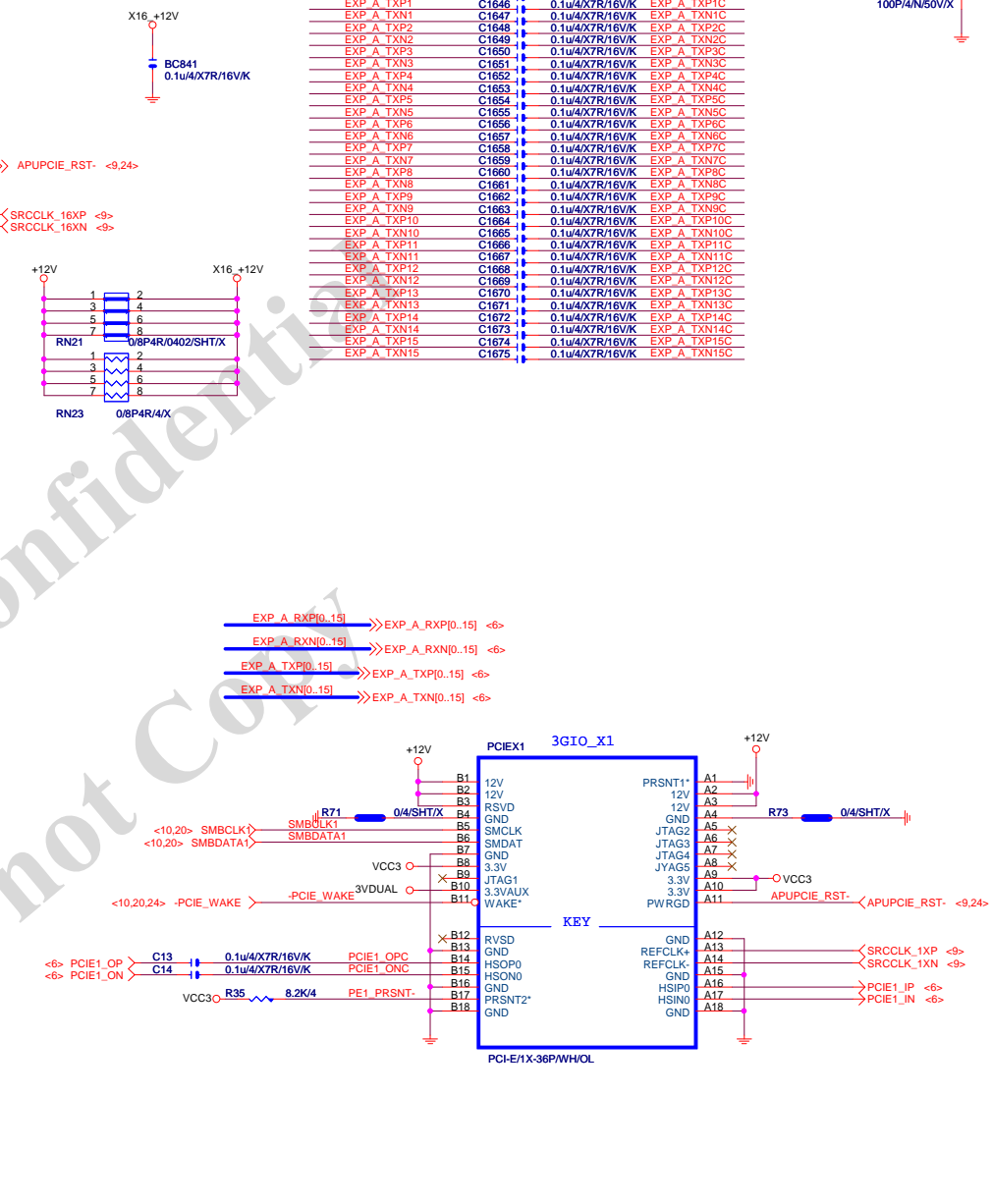
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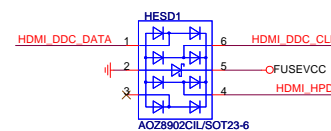
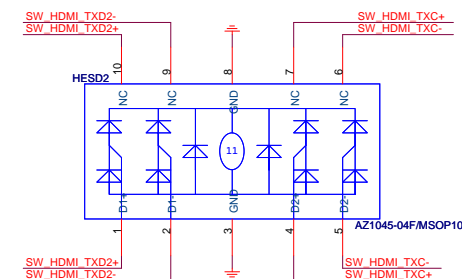
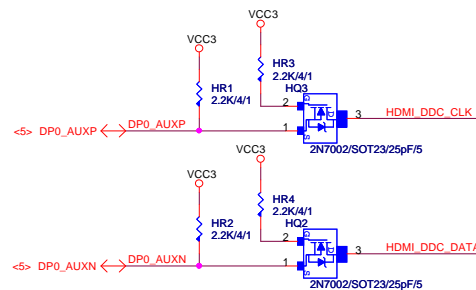
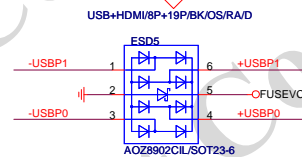


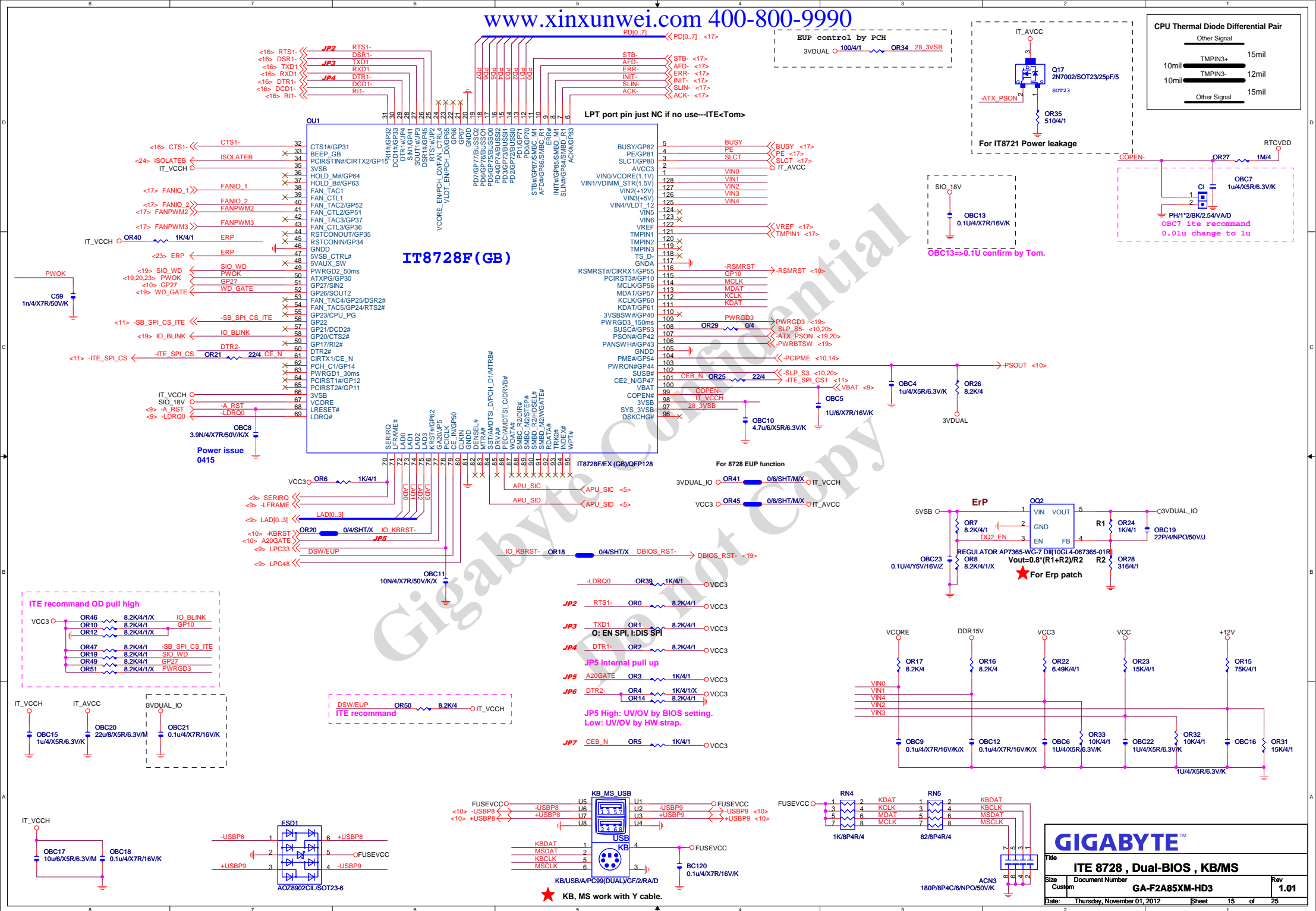
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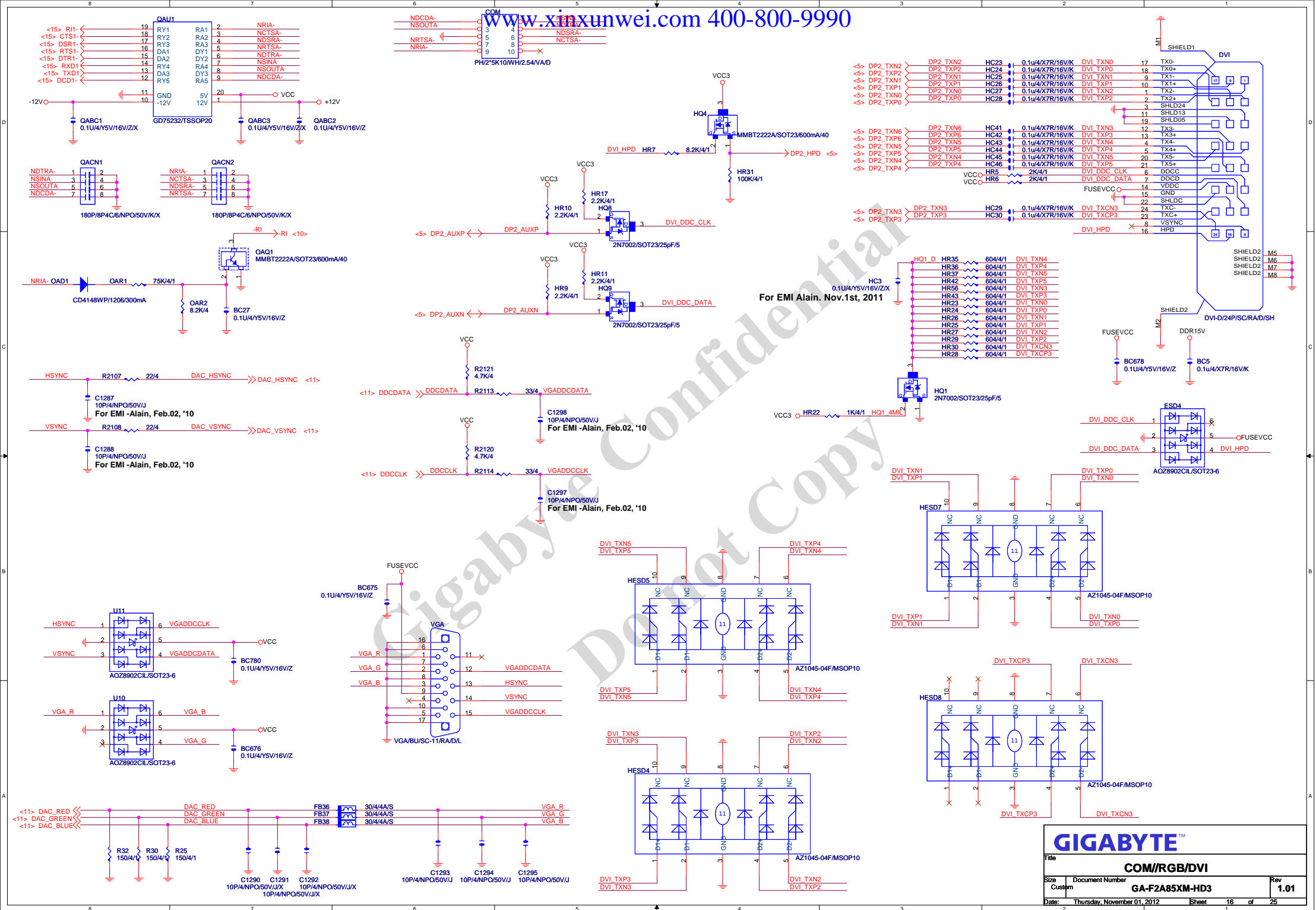




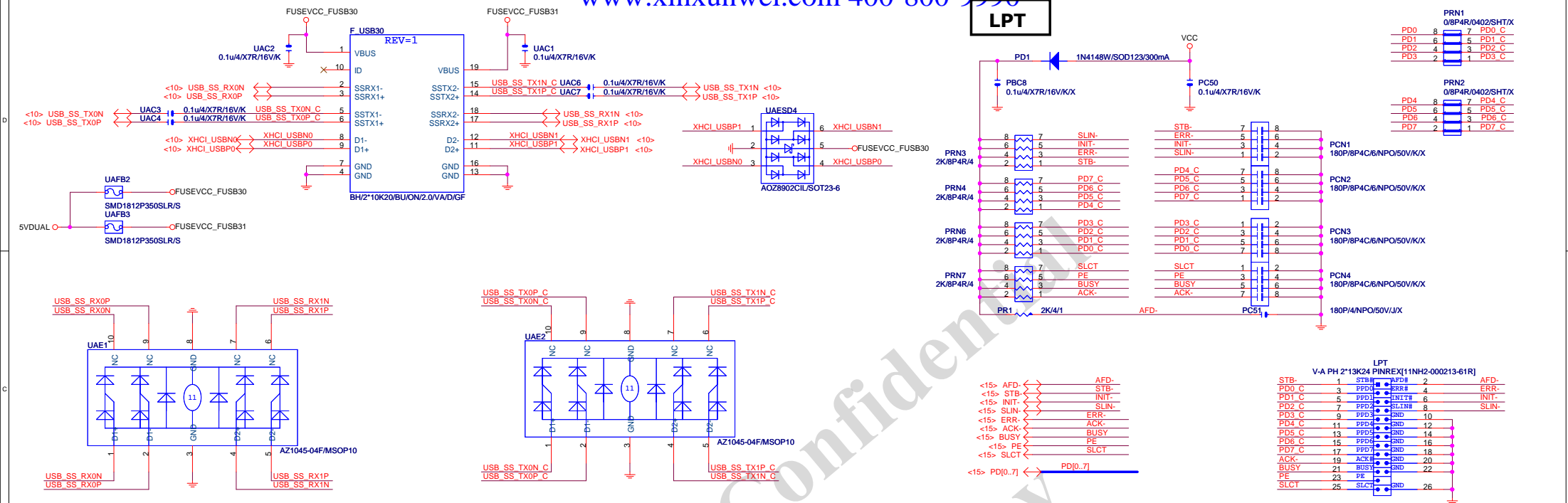




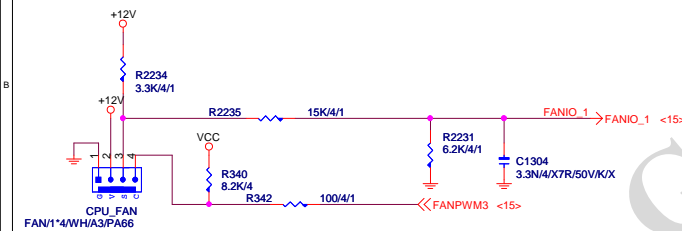




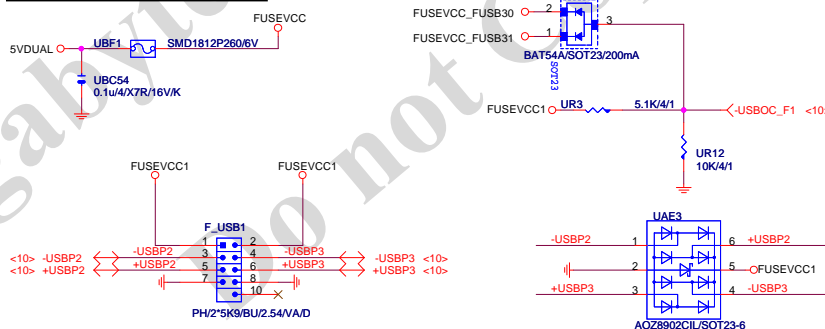
LPT



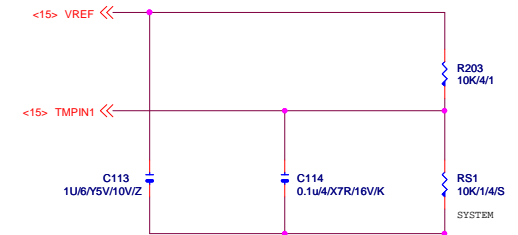
CPU FAN



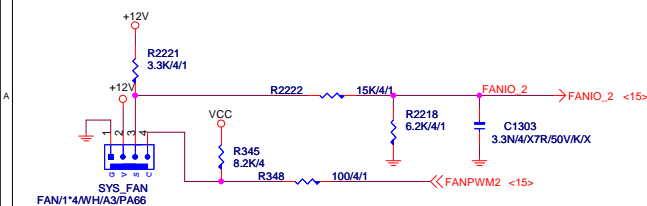
FRONT SIDE USB1



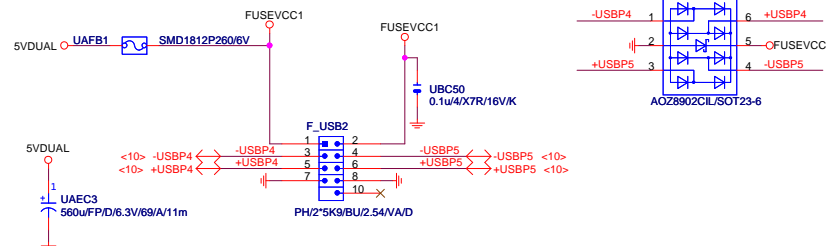
Hardware Monitor circuits



SYSTEM FAN

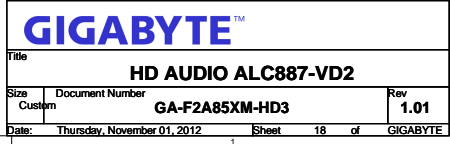


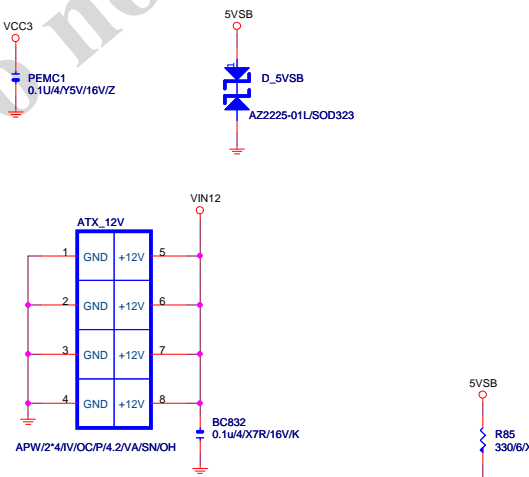
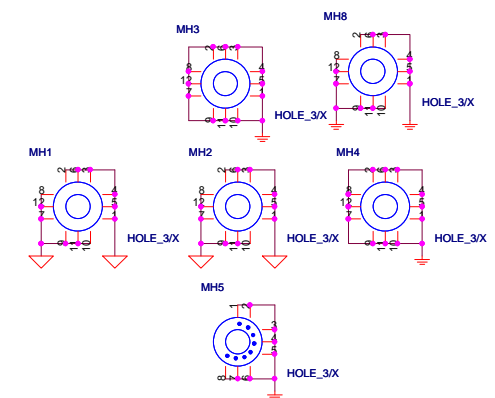
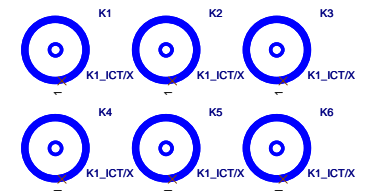
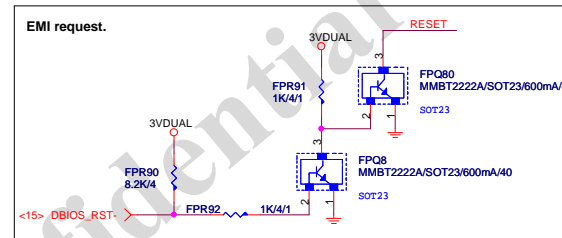
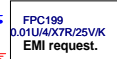
FRONT SIDE USB2

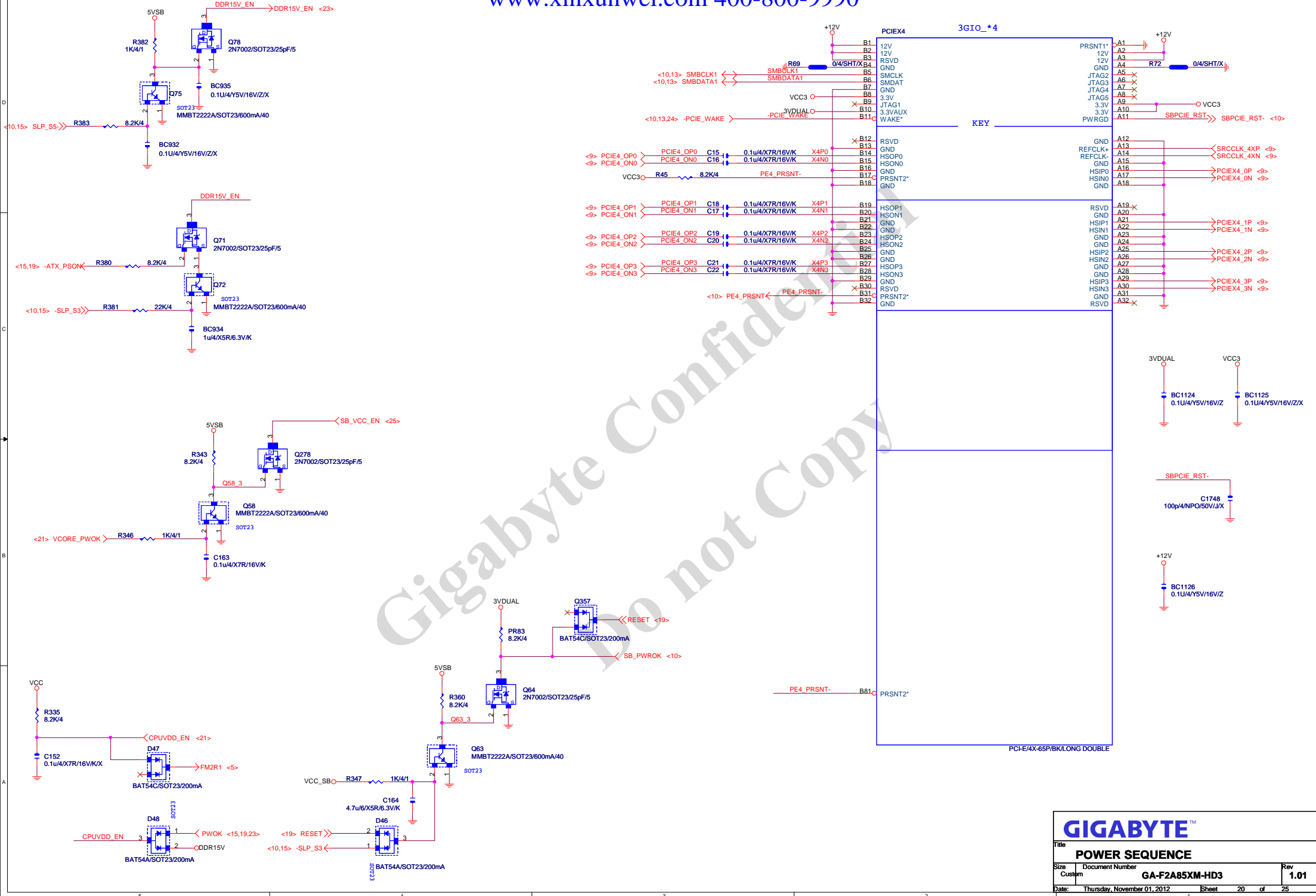


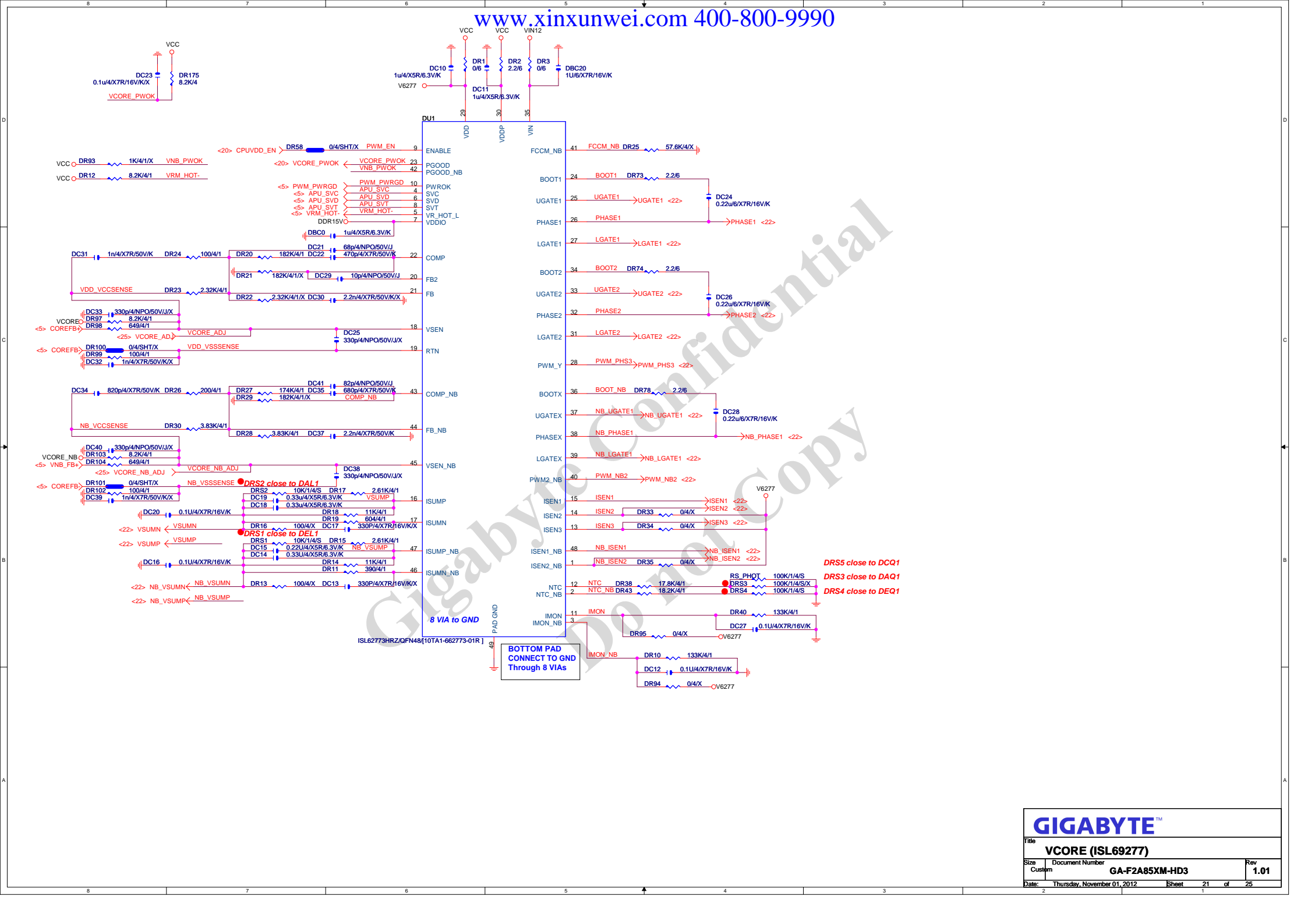
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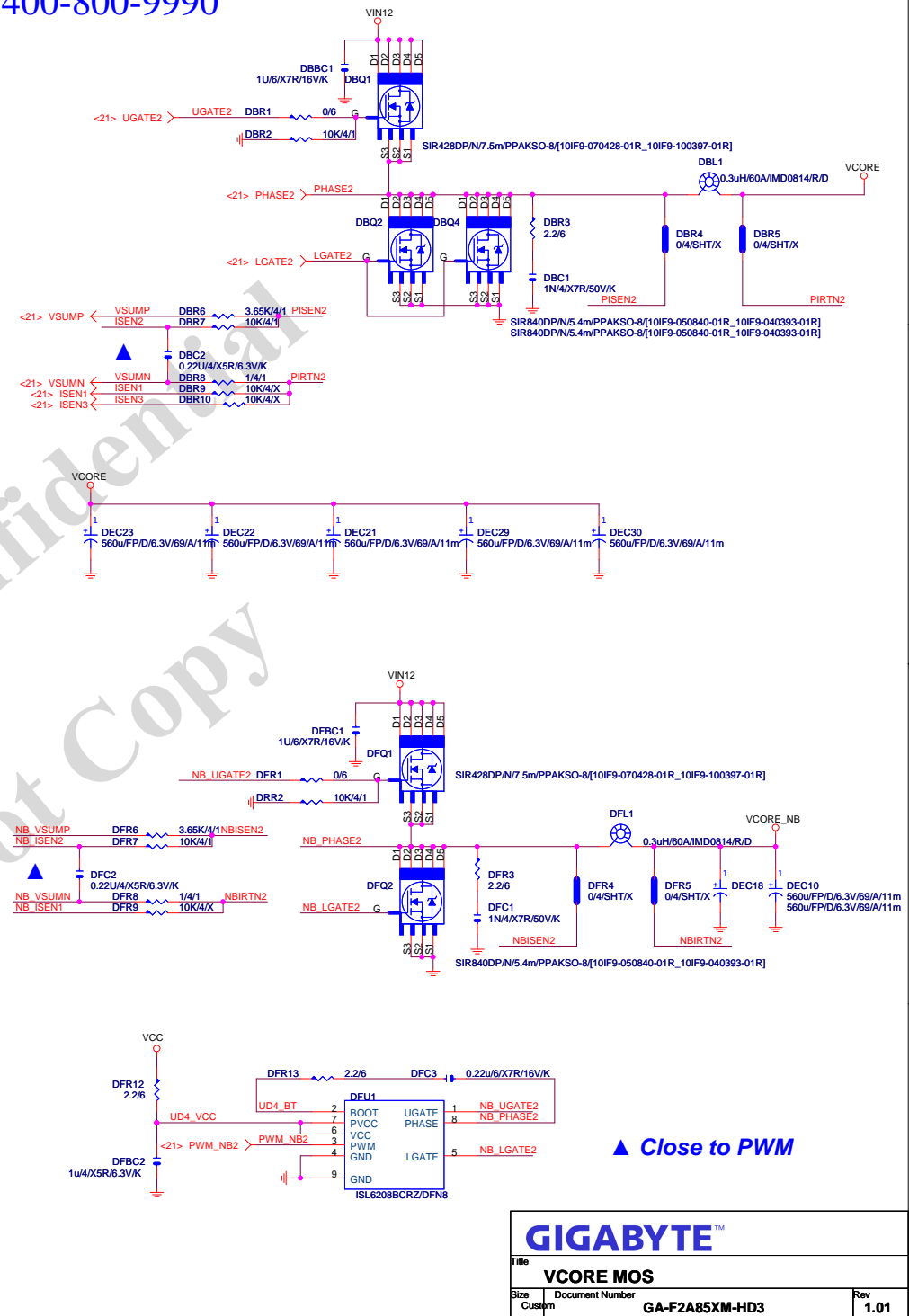
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FAN , HWMO , R_USB		
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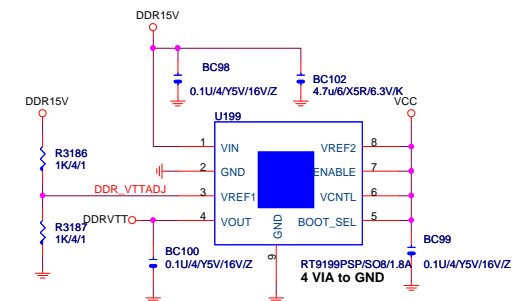
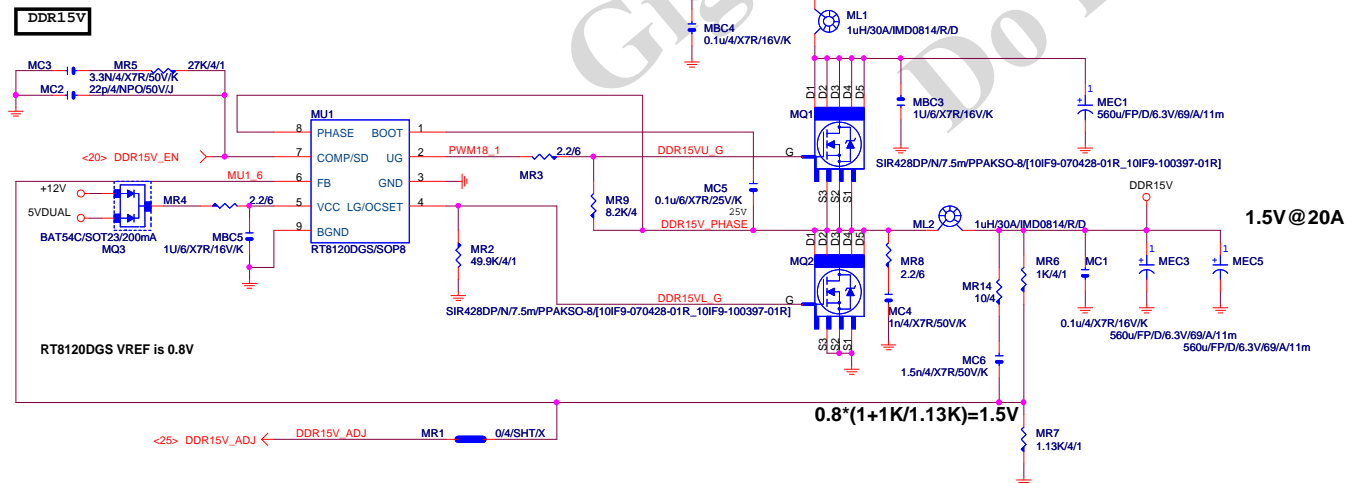
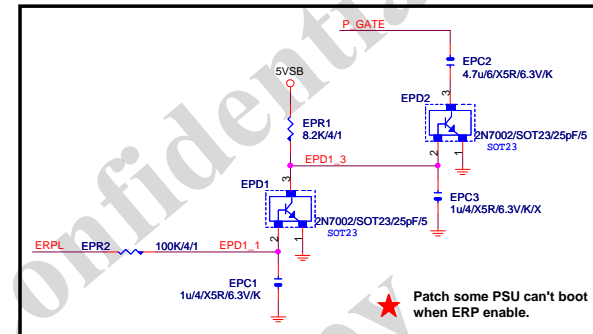
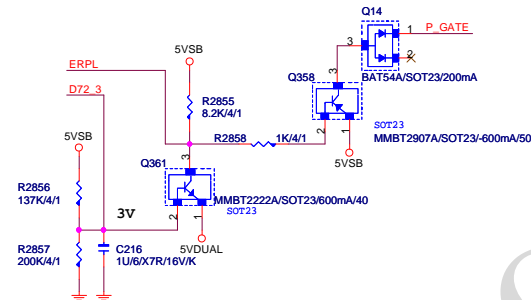
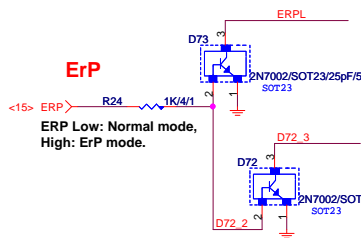
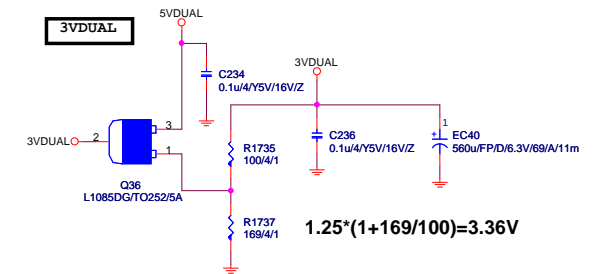
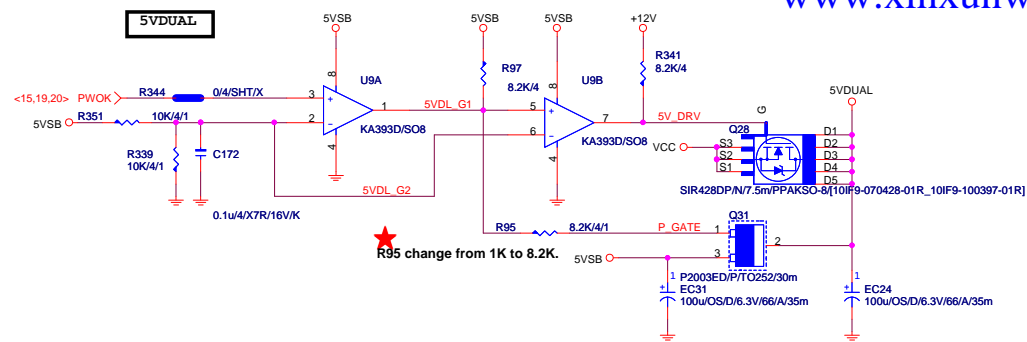








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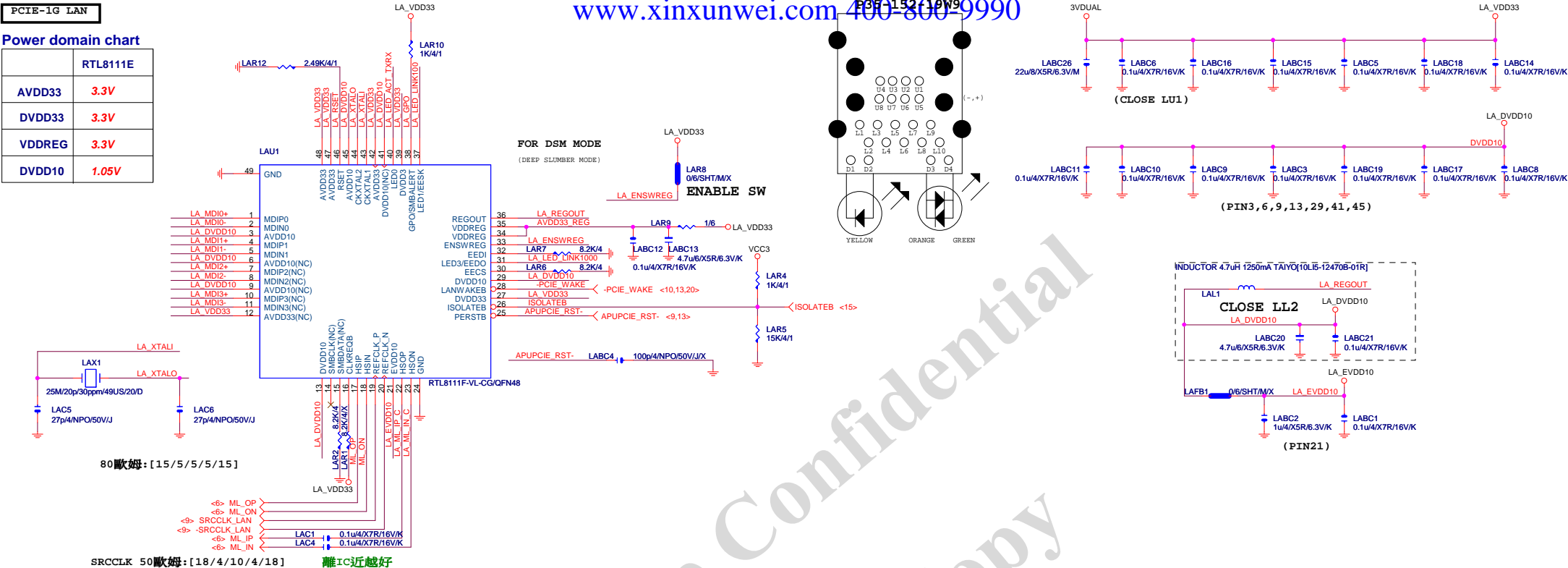


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DDR POWER, 5VDUAL, ErP		
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Power domain chart

	RTL8111E
AVDD33	3.3V
DVDD33	3.3V
VDDREG	3.3V
DVDD10	1.05V



USB_LAN

